

## AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph at page 14, lines 16 and 17 with the following amended paragraph:

Fig. 3 is a block diagram showing the internal structure of a memory controller [[205]] 25.

Please replace the paragraph at page 14, lines 18 and 19 with the following amended paragraph:

Fig. 4 is a schematic drawing of the memory map for buffer memory [[206]] 26 in a first embodiment of the invention.